

CLAIMS:

1. A display unit (1) comprising:
 - a display panel (50) with a bi-stable pixel (11) coupled to a predefined line via a capacitance (13,14); and
 - means (30,40) for reducing a voltage difference across the pixel (11)
- 5 resulting from a voltage-jump on the predefined line.
2. A display unit (1) as claimed in claim 1, wherein the pixel (11) is coupled via a switching element (12) to a line neighbouring the predefined line, with the capacitance (13) comprising a storage capacitor.
- 10 3. A display unit (1) as claimed in claim 2, wherein the means (30,40) comprise line driving circuitry (40) and data driving circuitry (30) for supplying a data signal to pixels (11) in at least two non-neighbouring lines simultaneously for the reducing of the voltage difference.
- 15 4. A display unit (1) as claimed in claim 1, wherein the pixel (11) is coupled to a switching element (12), with the capacitance (14) comprising a parasitic capacitor of the switching element (12).
- 20 5. A display unit (1) as claimed in claim 4, wherein the means (30,40) comprise line driving circuitry (40) and data driving circuitry (30) for supplying a data signal to pixels (11) in at least two lines simultaneously for the reducing of the voltage difference.
- 25 6. A display unit (1) as claimed in claim 4, wherein the means (30,40) comprise line driving circuitry (40) for driving at least two lines simultaneously at a reduced amplitude for the reducing of the voltage difference.
7. A display unit (1) as claimed in claim 4, wherein the predefined line is a storage line (25) coupled via storage capacitors (13) to pixels (11), with the means

comprising storage line driving circuitry for driving the storage line (25) for the reducing of the voltage difference.

8. A display unit (1) as claimed in claim 1, wherein the voltage difference is
5 reduced at the start and/or the end of an image update time-interval.

9. A display unit (1) as claimed in claim 1, further comprising a controller (20), which is adapted to provide:

10 - shaking data pulses (Sh_1, Sh_2);
- one or more reset data pulses (R); and
- one or more driving data pulses (Dr);
to the pixels (11).

10. A display device comprising a display unit (1) as claimed in claim 1 and
15 further comprising a storage medium for storing information to be displayed.

11. A method for driving a display unit (1) comprising a display panel (50) with a bi-stable pixel (11) coupled to a predefined line via a capacitance (13,14), which method comprises the step of reducing a voltage difference across the pixel (11) resulting from a
20 voltage-jump on the predefined line.

12. A processor program product for driving a display unit (1) comprising a display panel (50) with a bi-stable pixel (11) coupled to a predefined line via a capacitance (13,14), which processor program product comprises the function of reducing a voltage
25 difference across the pixel (11) resulting from a voltage-jump on the predefined line.